

Claims 1 and 5 stand rejected under 35 U.S.C. §103(a) as being unpatentable over an article by J.J. Chang et al. entitled “Fully Differential Current-Input CMOS Amplifier Front-End Suppressing Mixed Signal Substrate Noise For Optoelectronic Applications” (hereinafter “Chang”), in view of an article by M. Xu et al. entitled “Measuring and Modeling the Effects of Substrate Noise on the LNA for a CMOS GPS Receiver” (hereinafter “Xu”).

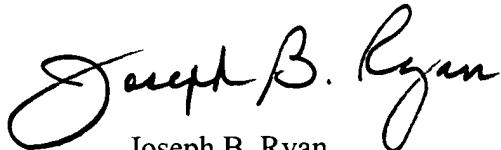
Applicants respectfully traverse the §103(a) rejection on the ground that the Examiner has failed to establish that the Xu article is prior art relative to the present application.

As indicated above, the present application claims the priority of U.S. provisional patent application Serial No. 60/270,263 filed February 21, 2001. Applicants note that the Examiner has failed to acknowledge this priority claim in the appropriate section on page 1 of the Office Action. Moreover, the Xu article on page 473 thereof bears an apparent publication date of March, 2001, which is subsequent to the effective filing date of the present application.

Thus, it is believed that the Examiner has failed to meet his initial burden of establishing that the Xu article constitutes prior art relative to the present application. The §103(a) rejection over a proposed combination of Chang and Xu is therefore improper.

In view of the above, Applicants believe that claims 1 and 5 are in condition for allowance, and respectfully request the withdrawal of the §103(a) rejection.

Respectfully submitted,



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Enclosure(s): Copy of Formal Drawings filed May 14, 2002
Copy of Transmittal of Formal Drawings